

REMARKS

Amendment To Claims

While Applicant traverses the rejections of the claims, in order to advance the prosecution of this application, Applicant is amending independent Claims 1, 9, 19, and 28, and canceling dependent Claims 4, 13, 23 and 32, without prejudice or disclaimer.

More specifically, Applicant is amending independent Claims 1, 9, 19, and 28 to recite the features of:

“wherein the EL element comprises:

- a first pixel comprising a blue luminescent layer,
- a second pixel comprising a green luminescent layer, and
- a third pixel comprising a red luminescent layer.”

As these features were previously recited in dependent Claims 4, 13, 23, and 32 (now canceled), no new matter is being added. Therefore, it is respectfully requested that this amendment be entered and allowed.

Applicant now has the following response to rejections in the Office Action.

Claim Rejections - 35 USC §103

In the Office Action, the Examiner has the following rejections under 35 USC §103(a):

1. Claims 1-3, 9-12, 18-22 and 28-31 are rejected as being unpatentable over Yamada et al. (US 5,990,629) in view of Shioya et al. (US 6,091,382) in view of von Stein et al. (US 6,529,243).
2. Claims 7, 16, 26 and 35 are rejected as being unpatentable over Yamada et al. and Shioya and von Stein et al. in view of Yamazaki et al. (US 6,388,652).
3. Claims 8, 17, 27 and 36 are rejected as being unpatentable over Yamada et al. and

Shioya et al. and von Stein et al. in view of Yamazaki et al. '652 and further in view of Yamazaki et al. (US 6,445,005).

4. Claims 4, 13, 23 and 32 are rejected as being unpatentable over Yamada et al. and Shioya et al. and von Stein et al. in view of Yamazaki et al. '652 and further in view of Choi et al. (US 6,583,577).

These rejections are respectfully traversed.

While Applicant traverses these rejections, as explained above, Applicant has amended independent Claims 1, 9, 19 and 28. As the Examiner admits (in the rejection of Claims 4, 13, 23 and 32 whose features are now incorporated into the independent claims), neither Yamada, Shioya, von Stein, nor Yamazaki et al. '652 disclose the above added features of independent Claims 1, 9, 19 and 28.

The Examiner cites Choi as allegedly disclosing these claimed features. It is respectfully submitted that Choi is not prior art to the present application.

More specifically, Choi issued on June 24, 2003 based on a U.S. filing date of August 18, 2000.¹

The present application was filed on September 20, 2000 and claims priority under 35 USC §119 of Japanese patent application serial number 11-271235 filed September 24, 1999 in Japan. A certified copy of this priority Japanese application was filed September 20, 2000 in the present U.S. application. Applicant is preparing a verified English translation of Japanese patent application serial number 11-271235 and will submit it as soon as it is finished. It is respectfully requested that the Examiner not issue a new action in this application until the verified English translation of Japanese patent application serial number 11-271235 has been filed.

¹ It is noted that Choi is based on KR 99-40743. This Korean application was published on April 6, 2001 as KR2001-28479 (see attached family list). This publication date is after the US filing date of the present application. Therefore, this document is also not prior art to the present application.

Hence, as the §119 priority filing date of the present application is prior to the U.S. filing date of Choi, Choi is not prior art to the present application.

In addition, it is respectfully submitted that Yamazaki '005 is not prior art to the present application.

More specifically, Yamazaki '005 issued on September 3, 2002 based on a U.S. filing date of September 13, 2000.

As explained above, the present application was filed on September 20, 2000 and claims priority under 35 USC §119 of Japanese patent application serial number 11-271235 filed September 24, 1999 in Japan. As also explained above, a certified copy of this priority Japanese application was filed September 20, 2000 in the present U.S. application, and Applicant is preparing a verified English translation of Japanese patent application serial number 11-271235 and will submit it as soon as it is finished.

Hence, as the §119 priority filing date of the present application is prior to the U.S. filing date of Yamazaki '005, Yamazaki '005 is not prior art to the present application.

Accordingly, for at least the above-stated reasons, it is respectfully requested that these rejections be withdrawn.

Information Disclosure Statement

Applicant is submitting an information disclosure statement (IDS) herewith. It is respectfully requested that this IDS be entered and considered prior to the issuance of any further action on this application.

Conclusion

It is respectfully submitted that the present application is in a condition for allowance and should be allowed.

Please charge our deposit account 50/1039 for any fee due for this amendment.

Favorable reconsideration is earnestly solicited.

Date: September 23, 2008

Respectfully submitted,

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Family list

2 family members for: **US6583577**

Derived from 2 applications

[Back to US6583577](#)

1 ELECTROLUMINESCENT DISPLAY DEVICE AND ITS DRIVING METHOD

Inventor: BAE SEONG JUN (KR); CHOI UNG SIK (KR) **Applicant:** LG PHILIPS LCD CO LTD (KR)

EC: G09G3/32; H01L27/32C4; (+2)

IPC: H05B33/08; G09G3/32; H01L27/32 (+4)

Publication info: **KR20010028479 A** - 2001-04-06

2 Electro-luminescent display and driving method thereof

Inventor: CHOI WOONG-SIK (KR); BAE SUNG-JOON (KR) **Applicant:** LG PHILIPS LCD CO LTD (KR)

EC: G09G3/32; H01L27/32C4; (+2)

IPC: H05B33/08; G09G3/32; H01L27/32 (+4)

Publication info: **US6583577 B1** - 2003-06-24

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Family list13 family members for: **US6445005**

Derived from 10 applications

[Back to US6445005](#)

- 1 EL display device and method for making same**
Inventor: SHUNPEI YAMAZUKI (JP); MAYUMI MIZUKAMI (JP); (+1)
EC: H01L51/52C; H01L27/32M2; (+1)
Publication info: **CN1289151 A** - 2001-03-28
CN100383983C C - 2008-04-23
Applicant: SEMICONDUCTOR ENERGY LAB (JP)
IPC: *H01L31/12; G09G3/30; H01L27/32* (+14)
- 2 EL display device and method for manufacturing the same**
Inventor: MIZUKAMI MAYUMI (JP); KONUMA TOSHIMITSU (JP); (+1)
EC: H01L51/52C; H01L27/32M2; (+1)
Publication info: **EP1085576 A2** - 2001-03-21
EP1085576 A3 - 2004-04-28
Applicant: SEMICONDUCTOR ENERGY LAB (JP)
IPC: *H01L31/12; G09G3/30; H01L27/32* (+13)
- 3 EL DISPLAY DEVICE AND ITS MANUFACTURING METHOD**
Inventor: YAMAZAKI SHUNPEI; MIZUKAMI MAYUMI; (+1)
EC: H01L27/32M2; H01L51/52C; (+1)
Publication info: **JP2001217072 A** - 2001-08-10
Applicant: SEMICONDUCTOR ENERGY LAB
IPC: *G03B17/18; G09F9/00; G09F9/30* (+23)
- 4 EL DISPLAY DEVICE AND MANUFACTURING METHOD FOR THE SAME**
Inventor: KONUMA TOSHIMITSU; MIZUKAMI MAYUMI; (+1)
EC: H01L51/52C; H01L27/32M2; (+1)
Publication info: **KR20010067183 A** - 2001-07-12
Applicant: SEMICONDUCTOR ENERGY LAB KK
IPC: *H01L31/12; G09G3/30; H01L27/32* (+12)
- 5 A DISPLAY DEVICE**
Inventor: YAMAZAKI SHUNPEI (JP); MIZUKAMI MAYUMI (JP); (+1)
EC: H01L51/52C; H01L27/32M2; (+1)
Publication info: **KR20050096892 A** - 2005-10-06
Applicant: SEMICONDUCTOR ENERGY LAB KK (JP)
IPC: *H01L31/12; G09G3/30; H01L27/32* (+14)
- 6 EL display device and method for manufacturing the same**
Inventor: YAMAZAKI SHUNPEI (JP); MIZUKAMI MAYUMI (JP); (+1)
EC: H01L51/52C; H01L27/32M2; (+1)
Publication info: **TW516244B B** - 2003-01-01
Applicant: SEMICONDUCTOR ENERGY LAB (JP)
IPC: *H01L31/12; G09G3/30; H01L27/32* (+12)
- 7 Display device**
Inventor: YAMAZAKI SHUNPEI (JP); MIZUKAMI MAYUMI (JP); (+1)
EC: H01L51/52C; H01L27/32M2; (+1)
Publication info: **TW522453B B** - 2003-03-01
Applicant: SEMICONDUCTOR ENERGY LAB (JP)
IPC: *H01L31/12; G09G3/30; H01L27/32* (+12)
- 8 EL display device**
Inventor: YAMAZAKI SHUNPEI (JP); MIZUKAMI MAYUMI (JP); (+1)
EC: H01L51/52C; H01L27/32M2; (+1)
Publication info: **US6445005 B1** - 2002-09-03
Applicant: SEMICONDUCTOR ENERGY LAB (US)
IPC: *H01L31/12; G09G3/30; H01L27/32* (+13)
- 9 EL DISPLAY DEVICE**
Inventor: YAMAZAKI SHUNPEI (JP); MIZUKAMI MAYUMI (JP); (+1)
EC: H01L27/32
Publication info: **US6894312 B2** - 2005-05-17
US2002190257 A1 - 2002-12-19
Applicant: SEMICONDUCTOR ENERGY LAB (US)
IPC: *H01L27/32; H01L27/28; (IPC1-7): H01L29/04*
- 10 EL display device and method for manufacturing the same**
Inventor: YAMAZAKI SHUNPEI (JP); MIZUKAMI MAYUMI (JP); (+1)
EC: H01L27/32
Publication info: **US2005162092 A1** - 2005-07-28
Applicant: SEMICONDUCTOR ENERGY LAB
IPC: *H01L27/32; H01L27/28; (IPC1-7): G09G3/12*